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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/673,158	09/30/2003	Taro Naoi	107156-00204	9823

7590 12/30/2004
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EXAMINER HODGES, MATTHEW P	
ART UNIT 2879	PAPER NUMBER

DATE MAILED: 12/30/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/673,158

Applicant(s)

NAOI ET AL.

Examiner

Matt P Hodges

Art Unit

2879

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-7 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 30 September 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
- 1) ☒ Certified copies of the priority documents have been received.
 - 2) ☐ Certified copies of the priority documents have been received in Application No. ____.
 - 3) ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 9/30/2004.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: ____.

DETAILED ACTION

Specification

The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1 and 2 are rejected under 35 U.S.C. 102(e) as being anticipated by Shinoda et al. (US 2003/0141817 A1).

Regarding claim 1, Shinoda discloses (see figure 3) a plasma display panel including a front substrate (1), row electrodes (21), a dielectric layer (5) covering the row electrodes on the front substrate, a back substrate (7), column electrodes (8) formed on the back substrate, partition walls (13) extending in a row direction, and black light absorption layers (14) between the cells formed by the partition walls. (Page 3 paragraph 31).

Regarding claim 2, the barrier walls include an interstice which is filled by the black absorption layer. (See figure 3).

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Claims 1 and 3 are rejected under 35 U.S.C. 102(e) as being anticipated by Masuda et al. (US 6,614,183).

Regarding claim 1, Masuda discloses (see figure 16) a plasma display panel including a front substrate (10), row electrodes (X and Y), a dielectric layer (11) covering the row electrodes on the front substrate, a back substrate (13), column electrodes (D) formed on the back substrate, partition walls (35B) extending in a row direction, and black light absorption layers (35') between the cells formed by the partition walls.

Regarding claim 3, the black absorption layer is formed on the barrier walls facing the first substrate. (See figure 16).

Claims 1, 2, and 4-7 are rejected under 35 U.S.C. 102(e) as being anticipated by Otani et al. (US 6,674,238).

Regarding claim 1, Otani discloses (see figure 8) a plasma display panel including a front substrate (10), row electrodes (X and Y), a dielectric layer (11) covering the row electrodes on the front substrate, a back substrate (13), column electrodes (D) formed on the back substrate, partition walls (15A and 15B) extending in a row direction, and light absorption layers (30) between the cells formed by the partition walls. (Column 22 lines 30-57).

Regarding claim 2, the barrier walls include an interstice which is filled by the absorption layer. (See figure 8).

Regarding claim 4, Otani further discloses (See figure 9) the use of partition walls separating the interstices into cells.

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Regarding claim 5, Otani further discloses (See figure 8) the use a black colored additional part (12) protruding into the discharge space from a portion of the back face of the dielectric layer opposite to the transverse walls.

Regarding claim 6, Otani further discloses that the space (C2) is used as a discharge space for creating electron emission.

Regarding claim 7, Otani further discloses the use of material of a high coefficient of secondary electron emission as the light absorption material (30). (Column 22 lines 30-57).

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Sano et al. (US 2002/0021090 A1) discloses the use of black absorption material in various patterns around the discharge cell.

Okada et al. (US 2004/0124775 A1) discloses the use of secondary cells for electron emission next to the discharge cell.

Contact Information


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Matt P Hodges whose telephone number is (571) 272-2454. The examiner can normally be reached on 7:30 AM to 4:00 PM M-F.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nimesh Patel can be reached on (571) 272-2457. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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